L Number	Hits	Search Text	DB	Time stamp
1	134	"semiconductor chip" and "memory chip" and	USPAT;	2004/07/20 14:46
		"peripheral circuit" and dedicat\$3	US-PGPUB;	
	1		EPO; JPO;	
	!		DERWENT;	
l i			IBM TDB	
2	2	("semiconductor chip" and "memory chip" and	USPAT;	2004/07/20 14:48
	_	"peripheral circuit" and dedicat\$3) and	US-PGPUB;	
		"first chip" and "second chip"	EPO; JPO;]
		Little only and booms only	DERWENT;	
			IBM TDB	
3	5	("semiconductor chip" and "memory chip" and	USPAT:	2004/07/20 14:48
	, ,	"peripheral circuit" and dedicat\$3) and	US-PGPUB;	2001,01,20 11110
l		"design rule"	EPO; JPO;	
1		design rare	DERWENT;	
			IBM TDB	
ì _	2	("5969383").PN.	USPAT;	2004/06/11 15:31
		(3303303).EM.	US-PGPUB;	2004/00/11 13:31
1	ļ		EPO; JPO;	
	ļ		DERWENT;	
			IBM TDB	
	2	("5969383").PN.	USPAT;	2004/06/10 13:13
-		("5969363").PN.	US-PGPUB;	2004/06/10 13:13
			EPO; JPO;	1
			DERWENT;	
			IBM TDB	
	4	(("6741486") or ("6594169")).PN.	USPAT;	2004/07/20 07:35
-	4	(("6/41486") OI ("6594169")).PN.	US-PGPUB;	2004/07/20 07:35
			EPO; JPO;]]
			DERWENT;	1
	1		IBM TDB	
_	4944	"first memory" and "second memory" and	USPAT;	2004/07/20 07:37
-	4,744	peripheral	US-PGPUB:	2004/07/20 07:37
		borthuorar	EPO; JPO;	
			DERWENT;	
	1		IBM TDB	
<u> </u>	66	"first memory" same "second memory" same	USPAT;	2004/07/20 09:57
		"peripheral circuit"	US-PGPUB;	2004,01,20 03.31
		polipholai olicale	EPO; JPO;	1
			DERWENT;	
	1		IBM TDB	
_	5	"first memory" same "second memory" same	USPAT;	2004/07/20 10:37
	, ,	peripheral same dedicate\$3	US-PGPUB;	2001, 01, 20 10.31
		perspectat bame acareaecys	EPO; JPO;	
			DERWENT;	
	,		IBM TDB	
_	1		USPAT	2004/07/20 10:03
_	1		USPAT	2004/07/20 10:05
_	1		USPAT	2004/07/20 10:05
			LOSERI	2001/01/20 10:03